

Diagnosis And Reliable Design Of Digital Systems

By Melvin A. Breuer;Arthur D. Friedman

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In this paper, we discuss a module diagnosis and design-for-debug methodology through hierarchical test paths.

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Prerequisite: ECE 620. Basic theory and techniques for testing VLSI circuits and systems. Fault Modeling, logic simulation and fault simulation techniques are discussed.

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Melvin A. Breuer is the author of Diagnosis & reliable design of digital systems 0 reviews, published 1976), Digital System D register;

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